

AF/2812

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PTO/SB/21 (08-00)

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Total Number of Pages in This Submission

Application Number

09/412,512

Filing Date

October 5, 1999

First Named Inventor

Shunpei YAMAZAKI

Group Art Unit

2812

Examiner Name

R. Booth

Attorney Docket Number

740756-2046

**ENCLOSURES (check all that apply)**☐ Fee Transmittal Form☐ Fee Attached☒ Amendment / Reply☒ After Final☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☐ Information Disclosure Statement☐ Certified Copy of Priority Document(s)☐ Response to Missing Parts/  
Incomplete Application☐ Response to Missing Parts  
under 37 CFR 1.52 or 1.53☐ Assignment Papers  
(for an Application)☐ Drawing(s)☐ Licensing-related Papers☐ Petition☐ Petition to Convert to a  
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Remarks

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Date

1-2-02

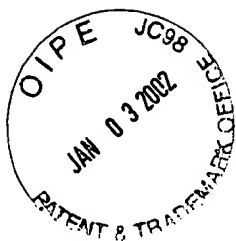
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BOX AFTER FINAL  
EXPEDITED PROCEDURE  
ART UNIT 2812

Docket No. 740756-2046

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application: )

Shunpei YAMAZAKI )

Application No.: 09/412,512 )

Filed: October 5, 1999 )

For: SEMICONDUCTOR DEVICE INCLUDING )

SEMICONDUCTOR CIRCUIT MADE FROM )

SEMICONDUCTOR ELEMENT AND )

MANUFACTURING METHOD THEREOF )

Art Unit: 2812

Examiner: R. Booth

#13/C (1/8/02)  
V. Vannae

AMENDMENT

Honorable Commissioner of Patents  
Washington, D.C. 20231

Sir:

Responsive to the Office Action dated **April 17, 2001**, the following amendments and remarks are submitted in connection with the above-identified application.

IN THE CLAIMS:

Please amend claims 14-19 as follows. Attached hereto is a marked-up copy of the amended claims.

14. (Amended) A method for manufacturing semiconductor device comprising a semiconductor circuit, said method comprising the steps of:
- forming a base film on a plastic substrate;
  - forming an amorphous semiconductor film through a sputtering method on the base film; and

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